

**BIOS: Basic Input/Output System** - A set of programs encoded in Read Only Memory (ROM) to facilitate the transfer of data and control instructions between the computer and peripherals.

**CMOS:** Complementary **Metal-Oxide Semiconductor** - An energy-saving chip fabricated to duplicate the functions of other chips, such as memory chips or microprocessors. The system settings for most PCs are stored in the CMOS.

**DLL:** the extension used by **D**ynamic **L**ink **L**ibrary files, which contain program code.

## Glossary

It is recommended that the help file be read from the [Readme.hlp](#).

**B**

**BIOS**

**C**

**CMOS**

**D**

**DLL**

**I**

**ISP**

**J**

**JRE**

**L**

**LAN**

**O**

**OEM**

**P**

**PCI**

**Q**

**QEMM**

**R**

**RAM**

**U**

**URL**

**V**

**VESA**

**VLB**

**ISP: Internet Service Provider**

**JRE: Java™ Runtime Environment**

**LAN:** Local Area Network

**OEM: Original Equipment Manufacturer**



**PCI: Peripheral Component Interconnect local bus** - This architecture is the successor to VLB.

**QEMM: Quarterdeck Expanded Memory Manager** from Quarterdeck Corp.

**RAM: Random Access Memory**

**URL: Uniform Resource Locator** - An addressing scheme used to link resources on the World Wide Web. Using URLs, a web browser can point to most resources on the net.

**VESA: Video Electronics Standards Association.**

**VLB: VESA Local Bus** - A high-speed bus for graphics cards that provides better video performance on high-end machines.

